Reviewed by:Andrew Newman

Module Name:Acceptance Filter

Jira Ticket:[CAN2-27]

**CAN Controller Requirements Checklist Rev.3**

1. Is requirements document written clearly and grammatically correct?

Yes [x] No [ ]

Comments:

1. Does requirements document define all functions to be performed by the module?

Yes [x] No [ ]

Comments:

1. Does requirements document include all inputs and outputs to the module along with their corresponding accuracy, range of values, frequency and format?

Yes [ ] No [x]

Comments:   
[ACCEPT\_FSM\_03 - \*\_06] Change to “The module shall transition FSM from state PROCESSING to state ACCEPTMESSAGE if mask\_id\_one[31:0] is not equal to 32’h00000000 and mask\_msg\_one[31:0] is not equal to 32’h00000000 and i\_uaf1 is logic high and mask\_id\_one{31:0] is equal to mask\_msg\_one[31:0}”

[ACCEPT\_FSM\_07] Change to transfer to ACCEPTMESSAGE instead.

[ACCEPT\_FSM\_08] Probably split into 4 reqs, one for each with following text(similar to accept\_fsm\_03 - \*\_06):

“The module shall transition FSM from state PROCESSING to state DISCARDMESSAGE if mask\_id\_one[31:0] is not equal to 32’h00000000 and mask\_msg\_one[31:0] is not equal to 32’h00000000 and i\_uaf1 is logic high and mask\_id\_one{31:0] is not equal to mask\_msg\_one[31:0}”

1. Is each requirement unique?

Yes [ ] No [ ]

Comments: The READY group of signals can be condensed to a single requirement.

1. Does requirements document comply with system requirements and traceability?

Yes [x] No [ ]

Comments:

1. Is terminology consistent with Requirements Standards?

Yes [x] No [ ]

Comments:

1. Is it possible to implement all requirements?

Yes [x] No [ ]

Comments:

1. Is each requirement testable or verifiable?

Yes [x] No [ ]

Comments: